

M88DDR4RCD02 & M88DDR4DB02

Gen2 DDR4 RCD & DB for LRDIMM

General Description

M88DDR4RCD02 is a Gen2 DDR4 Registering Clock Driver (RCD) and M88DDR4DB02 is a Gen2 DDR4 Data Buffer (DB) with differential strobes. The two chips are fully compliant with the JEDEC DDR4RCD02 and DDR4DB02 specifications. An RCD can be used in conjunction with nine DBs on an LRDIMM to provide advanced memory solutions with higher capacity, higher speed and lower power for DDR4 server platforms.

Applications

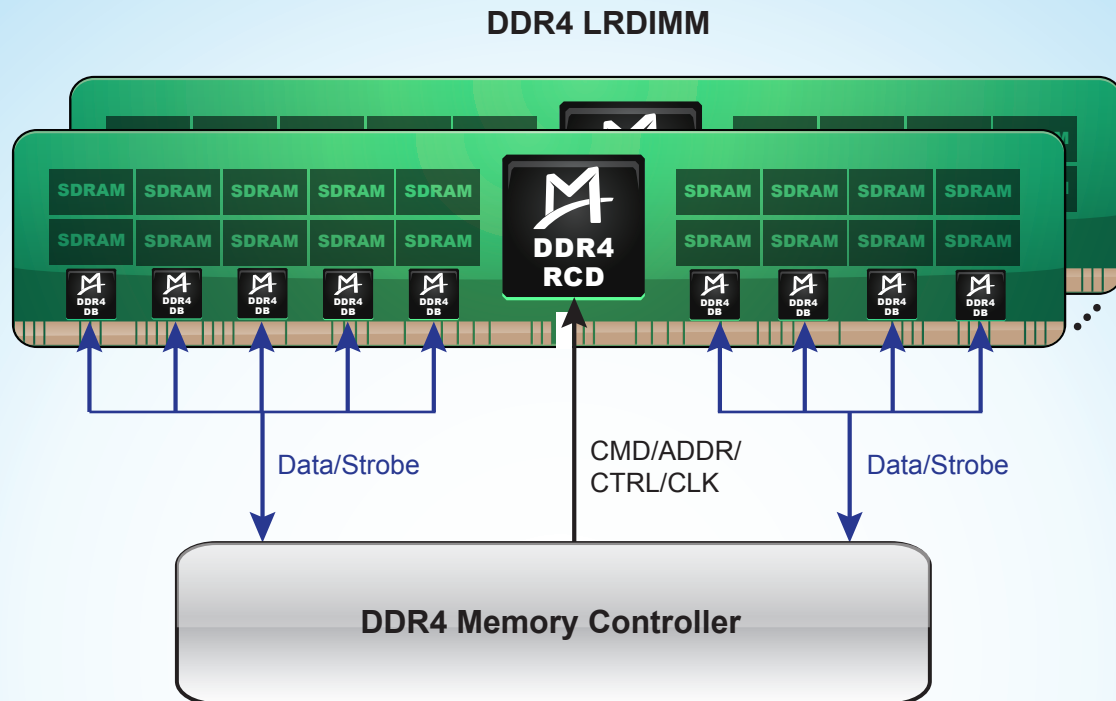
- DDR4 LRDIMM

Feature List

- Fully compliant with JEDEC DDR4RCD02 and DDR4DB02 specifications
- Speed up to DDR4-2666
- 1.2 V VDD voltage
- M88DDR4RCD02:
 - 32-bit 1:2 registering buffer for address and control signals
 - Integrated PLL clock driver distributing one differential clock pair to five differential pairs
 - Parity checking on command and address inputs
 - Green package: 253-ball Fine-Pitch Grid Array (FBGA)
- M88DDR4DB02:
 - Bidirectional retiming and 1:1 redriving of DDR4 data (DQ) signals
 - Only x4 DRAM devices supported
 - Parity error checking and sequence error checking on the BCOM interface
 - Green package: 53-ball Fine-Pitch Grid Array (FBGA)



Application Diagram



* Here shows the LRDIMM application as an example.

